CSIT 256-99V Computer Architecture & Assembly Language CRN 33627

Summer 2025 - Class Schedule 0.4

Section Info:

Section 99V – CRN 33627
Online

Version

- Version 0.4 – 6/6/2025 – Second Update – corrected references to S10/S12 on week 1
- Version 0.3 – 6/5/2025 – First Update (Corrected Arch. Lab #s and fixed some Friday dates)
- Version 0.2 – 6/5/2025 – First Release
- Version 0.1 - 5/18/2025 – Second Draft
- Version 0.0 - 3/5/2025 – First Draft

Consult the syllabus for Grade Determinants (what everything is worth), Compiling Policy, Late Policy (don't be late), and Cheating Policy (don't cheat)

Week #	Week of	Topic(s) (pdfs of slides in Canvas)	Work Due Tuesday	Work Due Friday
1	**6/9**	Course Overview Arch: S - Ch 1 Basic Conc. & Comp. Evolution ***(a)*** S - Ch 2 Performance Issues S - Ch 10 Number Systems ***(b)*** S - Ch 12 Digital Logic ***(b)*** Assembly: i - Ch 1. Basic Concepts ***(b)*** i - Ch 2. IA 32 Proc. Architecture ***(a)*** Note ***(a)*** Some Topics overlap S1 i2 ***(b)*** Some Topics overlap S10 S12 i1	6/10 **Arch. Lab i1 - conversions / Logic Tables** Assem. Lab i1 - Hello World	6/13 *Hw #1 - S1,S2* **Arch. Lab S10/12 - conversions / Logic Tables** Assem. Lab i2 - Register Dump+
2	**6/16**	Arch: S - Ch 3 A Top-Level View of Computer Function and Interconnection S - Ch 4 The Memory Hierarchy: Locality and Performance S - Ch 5 Cache Memory Assembly: i - Ch 3. Assembly Lang. Fund. i - Ch 4. Data Tran., Addr., + Arithmetic	6/17 *Hw #2 – S10,S12* **Arch. Lab S3 - hyp. machine** Assem. Lab i3- Variables	6/20 *Hw #3 - S3* **Arch. Lab S5 - Cache** Assem. Lab i4 - simp math

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3	**6/23**	*Exam 1 Information* Arch: S - Ch 6 Internal Mem. Technology S - Ch 7 External Mem. Assembly: i - Ch 5. Procedures i - Ch 6. Conditional Processing	6/24 *Hw #4 - S4/S5* **Arch. Lab S6 - Hamming Code** Assem. Lab i5 - Procedures	6/27 *Hw #5 - S6* Assem. Lab i6 - Cond. Proc. **Arch. Lab S7 - External Storage**
	6/30	**'Summer Break'**	n/a	n/a
4	**7/7**	**Exam 1 (15%)** Arch: S - Ch 8 Input/Output S - Ch 9 Operating System Support Assembly: i - Ch 7. Integer Arithmetic i - Ch 8. Advanced Procedures	7/8 **Exam 1 (15%)** *Hw #6 - S7* Assem. Lab i7 - iMUL and iDIV	7/11 *Hw #7 - S8* Assem. Lab i8 - Adv. Proc **Assembly Project 1 (i1-i5)**
5	**7/14**	Arch: S - Ch 11 Computer Arithmetic Assembly: i - Ch 9. Strings and Arrays	7/15 *Hw #8 - S9* Assem. Lab i9 - 2D Arrays	7/18 **Arch. Lab S11 – Booth**
6	**7/21**	*Exam 2 info* Arch: S - Ch 16 Processor Structure and Function Assembly: i - Ch 10. Structures and Macros	7/22 *Hw #9 - S11*	7/25 Assem. Lab i10 – STRUCT **Assembly Project 2 (i1-i7)**
7	**7/28**	***Exam 2 (25%)*** Arch: Assembly: i - Ch 12. Floating Point Processing and Instruction Encoding	7/29 ***Exam 2 (25%)***	8/1 *Hw #10 - S16*

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Week #	Week of	Topic(s) (pdfs of slides in Canvas)	Work Due Tuesday	Work Due Friday
		7/31 is the last day to withdraw		
8	**8/4**	*Final Exam Information* Arch: S - Ch 17 Reduced Instruction Set Computers (RISC) Assembly: i - Ch 11. MS-Windows prog	8/5 Assem. Lab i12 - floating point	8/8
9	**8/11**	**Assembly Project 3** ***Final Exam (30%) **** ***due 8/15 11:59 pm***	8/12 **Assembly Project 3 (i1-i10)**	8/15 ***Final Exam (30%)*** ***Due by*** ***11:59 pm***

***** In order to pass the course, you will need an overall average of 60 in the course and you will need a 60 or above on the average of Exam 2 and Final Exam.***

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