

CSIT 256-01 Computer Architecture & Assembly Language CRN 27557

Spring 2025 - Class Schedule 0.4

Section Info:

Section 01 – CRN 2755

Tuesday/Thursday 2:30 pm – 4:50 pm – W311

Version

- Version 0.4 – 3/31/2025 – changes to timing of Labs i8/i9 and i10/i12
- Version 0.3 – 3/16/2025 – moved Assem Lab for i7
- Version 0.2 – 1/13/2025 – corrected room
- Version 0.1 – 1/10/2025 – First Release
- Version 0.0 - 12/4/2024 – First Draft

Consult the syllabus for Grade Determinants (what everything is worth), Compiling Policy, Late Policy (don't be late), and Cheating Policy (don't cheat)

Class Schedule

| Week # | Date Tue Thur | Topic(s) (pdfs of slides in Canvas) | Work Due (details in Canvas) |
|--------|---------------------|--|---|
| 1 | 1/23 | Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Concepts i - Ch 1. Basic Concepts | 1/23 |
| 1 | 1/28 | S - Ch 1, Ch 2, cont. i - Ch 1, cont. | 1/28 Arch. Lab i1 - conversions / Logic Tables |
| 2 | 1/30 | S - Ch 1, Ch 2, cont. i - Ch 1, cont. | 1/30 Assem. Lab i1 - Hello World |
| 2 | 2/4 | S - Ch 10 Number Systems S - Ch 12 Digital Logic i - Ch 2. IA-32 Processor Architecture | 2/4 <i>Hw #1 - S1,S2</i> Arch. Lab S10/12 - conversions / Logic Tables |
| 3 | 2/6 | S - Ch 10, Ch 12, cont. i - Ch 2, cont. | 2/6 Assem. Lab i2 - Register Dump+ |
| 3 | 2/11 | S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund. | 2/11 <i>Hw #2 - S10,S12</i> Arch. Lab S3 - hyp. Machine |
| 4 | 2/13 | S - Ch 3, cont. i - Ch 3, cont. | 2/13 Assem. Lab i3- Variables |

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| Week # | Tue Thur | Topic(s) (pdfs of slides in Canvas) | Work Due (details in Canvas) |
|---------------|----------------------|--|---|
| 4 | 2/18 | S - Ch 4 The Memory Hierarchy: Locality and Performance S - Ch 5 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic | 2/18 <i>Hw #3 - S3</i> Arch. Lab S5 - Cache |
| 5 | 2/20 | S - Ch 4, Ch 5, cont. i - Ch 4, cont. | 2/20 Assem. Lab i4 - Simple Math |
| 5 | 2/25 | i - Ch 5. Procedures <i>Exam 1 Info</i> <i>Distribute Assembly Project 1 (i1-i5)</i> | 2/25 <i>Hw #4 - S4, S5</i> |
| 6 | 2/27 | i - Ch 5, cont. | 2/27 Assem. Lab i5 - Irvine Library and Procedures |
| 6 | 3/4 | S - Ch 6 Internal Mem. i - Ch 6. Cond Processing | 3/4 Arch. Lab S6 - Hamming Code |
| 7 | 3/6 | 2:30 – 3:20 Lab Time 3:20 - 3:30 Break 3:30 - 4:30 Exam 1 (15%) | 3/6 Exam 1 |
| | 3/11 3/13 | No Class - Spring Break | |
| 7 | 3/18 | S - Ch 7 External Mem. i - Ch 7. Integer Arithmetic <i>Distribute Assembly Project 2 (i1-i7)</i> | 3/18 <i>Hw #5 - S6</i> Arch. Lab S7 - External Storage |
| 8 | 3/20 | i - Ch 6, cont. i - Ch 7, cont. | 3/20 Assem. Lab i6 - Cond. Proc. |
| 8 | 3/25 | S - Ch 8 Input/Output i - Ch 8. Advanced Procedures | 3/25 <i>Hw #6 - S7</i> |
| 9 | 3/27 | Lab time <i>Note: during lab time, all questions must be asked in person</i> | 3/27 Assem. Lab i7 - iMUL and iDIV Assembly Project 1 (i1-i5) |

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|--------|-------------|---|---|
| 9 | 4/1 | S - Ch 9 Operating System Support i - Ch 9. Strings and Arrays | 4/1 Hw #7 - S8 Arch. Lab S9 – OS |
| 10 | 4/3 | Just Lab time for Assembly Project 2 <i>Note: during lab time, all questions must be asked in person</i> | 4/3 Assembly Project 2 (i1-i7) |
| 10 | 4/8 | S - Ch 11 Computer Arithmetic i - Ch 10. Structures and Macros <i>Exam 2 Info</i> <i>Distribute Assembly Project 3 (i1-i9)</i> | 4/8 Hw #8 - S9 Arch. Lab S11 - Booth's Alg. |
| 11 | 4/10 | i - Ch 10, cont. | 4/10 Assem. Lab i8 - Adv. Proc and i9 - 2D Arrays |
| | | Note: 4/10 last day to drop | |
| 11 | 4/15 | i - Ch 12. Floating Point prog. | 4/15 Hw #9 - S11 |
| 12 | 4/17 | 2:30 - 3:05 Lab Time 3:05-3:15 Break/Get Ready 3:15 - 4:45 Exam 2 (25%) | 4/17 Exam #2 |
| 12 | 4/22 | S - Ch 16 Processor Structure and Function | 4/22 |
| 13 | 4/24 | Just Lab time for Lab i12 and Assembly Project 3 <i>Note: during lab time, all questions must be asked in person</i> | 4/24 Assem. Lab i10 – STRUCT and i12 - floating point |
| 13 | 4/29 | i - Ch 11. MS-Windows prog <i>Final Exam Info</i> | 4/29 Hw #10 – S16 |
| 14 | 5/1 | Lab time for Assembly Project 3 <i>Note: during lab time, all questions must be asked in person</i> | 5/1 Assembly Project 3 (i1-i19) |
| 14 | 5/6 | S - Ch 17 Reduced Instruction Set Computers (RISC) | 5/6 |
| | | Note: Reading Day 5/7 | |

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|--------|-------------|---|---|
| | | Final Exam period 5/8 - 5/14 | |
| | 5/8 | 12:30-2:30 Final Exam (30%) | 5/8 Final Exam (30%) <i>(note time)</i> |

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