

# CSIT 256-01 Computer Architecture & Assembly Language CRN 27557

## Spring 2024 - Class Schedule 0.2

### Section Info:

Section 01 – CRN 27557

Tuesday/Thursday 2:30 pm – 4:50 pm – W309

### Version

- Version 0.2 – 2/14/2024 – Changes to reflect snow closure on 2/13
- Version 0.1 – 1/14/2024 – filled in topics
- Version 0.0 - 1/13/2024 - Empty Grid

**Consult the syllabus for Grade Determinants (what everything is worth), Compiling Policy, Late Policy (don't be late), and Cheating Policy (don't cheat)**

### Class Schedule

Week #	Date		Topic(s) ( pdfs of slides in Canvas)	Work Due ( details in Canvas )
	Tue	Thur		
1		1/18	Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Concepts i - Ch 1. Basic Concepts	1/18
1	1/23		S - Ch 1, Ch 2, cont. i - Ch 1, cont.	1/23 <b>Arch. Lab i1 - conversions / Logic Tables</b>
2		1/25	S - Ch 1, Ch 2, cont. i - Ch 1, cont.	1/25 Assem. Lab i1 - Hello World
2	1/30		S - Ch 10 Number Systems S - Ch 12 Digital Logic i - Ch 2. IA-32 Processor Architecture	1/30 <i>Hw #1 - S1,S2</i> <b>Arch. Lab S10/12 - conversions / Logic Tables</b>
3		2/1	S - Ch 10, Ch 12, cont. i - Ch 2, cont.	2/1 Assem. Lab i2 - Register Dump+
3	2/6		S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	2/6 <i>Hw #2 - S10,S12</i> <b>Arch. Lab S3 - hyp. Machine</b>
4		2/8	S - Ch 3, cont. i - Ch 3, cont.	2/8 Assem. Lab i3- Variables
4	2/13		No class due to snow closure	2/13

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<b>Week #</b>	<b>Tue Thur</b>	<b>Topic(s) ( pdfs of slides in Canvas)</b>	<b>Work Due ( details in Canvas)</b>
5	2/15	S - Ch 4 The Memory Hierarchy: Locality and Performance S - Ch 5 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	2/15 Hw #3 - S3 <b>Arch. Lab S5 - Cache</b>
5	2/20	i - Ch 4, cont. i - Ch 5. Procedures <i>Exam 1 Info</i>	2/20 Assem. Lab i4 - Simple Math
6	2/22	i - Ch 5, cont.	2/22 Hw #4 - S4, S5 Assem. Lab i5 - Irvine Library and Procedures
6	2/27	S - Ch 6 Internal Mem. i - Ch 6. Cond Processing	2/27 <b>Arch. Lab S6 - Hamming Code</b>
7	2/29	<b>Exam 1 (15%)</b>	2/29
	3/5 3/7	<b>No Class - Spring Break</b>	
7	3/12	S - Ch 7 External Mem. i - Ch 7. Integer Arithmetic	3/12 Hw #5 - S6 <b>Arch. Lab S7 - External Storage</b>
8	3/14	i - Ch 6, cont. i - Ch 7, cont.	3/14 Assem. Lab i6 - Cond. Proc.
8	3/19	S - Ch 8 Input/Output i - Ch 8. Advanced Procedures	3/19 Hw #6 - S7
9	3/21	i - Ch 7, cont.	3/21 Assem. Lab i7 - iMUL and iDIV
9	3/26	S - Ch 9 Operating System Support i - Ch 9. Strings and Arrays	3/26 Hw #7 - S8
10	3/28	i - Ch 9, cont.	3/28 Assem. Lab i8 - Adv. Proc and i9 - 2D Arrays
10	4/2	S - Ch 11 Computer Arithmetic i - Ch 10. Structures and Macros	4/2 Hw #8 - S9

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Week #	Tue Thur	Topic(s) ( pdfs of slides in Canvas)	Work Due ( details in Canvas)
		<i>Exam 2 Info</i>	<b>Arch. Lab S11 - Booth's Alg.</b>
11	4/4	i - Ch 10, cont.	4/4 Assem. Lab i10 – STRUCT
		<b>Note: 4/5 last day to drop</b>	
11	4/9	2:30 - 2:50 get ready <b>2:50 - 4:50 Exam 2 (25%)</b>	4/9 <b>Exam #2</b>
12	4/11	i - Ch 12. Floating Point prog.	4/12 <i>Hw #9 - S11</i>
12	4/16	i - Ch 12, cont.	4/16 Assem. Lab i12 - floating point
13	4/18	<b>Lab time for Assembly Project</b> <i>Note: during lab time, all questions must be asked in person</i>	4/18
13	4/23	<b>Lab time for Assembly Project</b> <i>See note on 4/18</i>	4/23
14	4/25	<b>Lab time for Assembly Project</b> <i>See note on 4/18</i>	4/25
14	4/30	<b>Lab time for Assembly Project</b> <i>See note on 4/18</i>	4/30 <b>Assembly Project ( 10% )</b>
		<b>Note: Reading Day 5/1</b> <b>Final Exam period 5/2 - 5/8</b>	
	5/2	<b>12:30-2:30</b> Final Exam (30%)	5/2 <b>Final Exam (x%)</b> <b>(note time)</b>

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