

CSIT 256-01 Computer Architecture & Assembly Language CRN 27557

Spring 2023 - Class Schedule 0.2

Section Info:

Section 01 CRN 27557

Tuesday / Thursday 2:30 - 4:50 pm W309

Version

- Version 0.2 - 3/29/2023 - changes because of no class on 3/16
- Version 0.1 - 3/1/2023 - changes because of missed class on 2/28 (moved Exam 1)
- Version 0.0 - 1/9/2023 - First Draft

Consult the syllabus for Grade Determinants (what everything is worth), Late Policy (don't be late), and Cheating Policy (don't cheat)

X

Class Schedule

S = Stallings Book / i = Irvine Book

Week #	Date		Topic(s) (pdfs of slides in Canvas)	Work Due	
	T	R		T	R
1		1/19	Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Concepts i - Ch 1. Basic Concepts		1/19
1	1/24		S - Ch 1, Ch 2, cont. i - Ch 1, cont.	1/24	Arch. Lab i1 - conversions / Logic Tables
2		1/26	S - Ch 1, Ch 2, cont. i - Ch 1, cont.		1/26 Assem. Lab i1 - Hello World
2	1/31		S - Ch 10 Number Systems S - Ch 12 Digital Logic i - Ch 2. IA-32 Processor Architecture	1/31	<i>Hw #1 - S1,S2</i> Arch. Lab S10/12 - conversions / Logic Tables
3		2/2	S - Ch 10, Ch 12, cont. i - Ch 2, cont.		2/2 Assem. Lab i2 - Register Dump+
3	2/7		S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	2/7	<i>Hw #2 - S10,S12</i> Arch. Lab S3 - hyp. machine
4		2/9	S - Ch 3, cont. i - Ch 3, cont.		2/9 Assem. Lab i3- Variables

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4	2/14	S - Ch 4 The Memory Hierarchy: Locality and Performance S - Ch 5 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	2/14 <i>Hw #3 - S3</i> Arch. Lab S5 - Cache
5	2/16	S - Ch 4, Ch 5, cont. i - Ch 4, cont.	2/16 Assem. Lab i4 - Simple Math
5	2/21	i - Ch 5. Procedures <i>Exam 1 Info</i>	2/21 <i>Hw #4 - S4, S5</i>
6	2/23	i - Ch 5, cont.	2/23 Assem. Lab i5 - Irvine Library and Procedures
6	2/28	No Class	2/28
7	3/2	S - Ch 6 Internal Mem. S - Ch 7 External Mem.	3/2 Arch. Lab S6 - Hamming Code Arch. Lab S7 - External Storage
	3/7 3/9	No Class - Spring Break	
7	3/14	3:00 - 4:00 Exam 1 (15%)	3/14 Exam 1 (15%)
8	3/16	No Class	3/16
8	3/21	i - Ch 6. Cond Processing i - Ch 7. Integer Arithmetic	3/21 <i>Hw #5 - S6</i> <i>Hw #6 - S7</i>
9	3/23	i - Ch 6, cont. i - Ch 7, cont.	3/23 Assem. Lab i6 - Cond. Proc. Assem. Lab i7 - iMUL and iDIV

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9	3/28	S - Ch 8 Input/Output S - Ch 9 Operating System Support i - Ch 8. Advanced Procedures	3/28
10	3/30	S - Ch 8/9, cont. i - Ch 8, cont.	3/30 Assem. Lab i8 - Adv. Proc.
10	4/4	S - Ch 11 Computer Arithmetic i - Ch 9. Strings and Arrays <i>Exam 2 Info</i>	4/4 <i>Hw #7 - S8 and Hw #8 - S9</i> Arch. Lab S8 - I/O Arch. Lab S11 - Booth's Alg.
11	4/6	S - Ch 11, cont. i - Ch 9, cont.	4/6 Assem. Lab i9 - 2D Arrays
Note: 4/7 last day to drop			
11	4/11	2:30 - 2:50 get ready 2:50 - 4:50 Exam 2 (15%)	4/11 Exam #2
12	4/13	S - Ch 16 Proc. Struct. and Func. i - Ch 10. Structures and Macros i - Ch 12. Floating Point prog.	4/13 <i>Hw #9 - S11</i> Assem. Lab i10 - STRUCT
12	4/18	S - Ch 17 Reduced Instruction Set Computers (RISC) i - Ch 11. MS-Windows prog <i>Final Exam Information</i>	4/18 <i>Hw #10 - S16</i> Assem. Lab i12 - floating point
13	4/20	Lab time for Assembly Project	4/20
13	4/25	Lab time for Assembly Project	4/25
14	4/27	Lab time for Assembly Project	4/27
14	5/2	Lab time for Assembly Project	5/2 Assembly Project (10%)
Note: Reading Day 5/3 Final Exam period 5/4 - 5/10			
	5/4	12:30-2:30 Final Exam	5/4 Final Exam (20%)

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