

CSIT 256-51 Computer Architecture & Assembly Language CRN 27557

Spring 2022

Class Schedule 0.1

Section Info:

Section 51 CRN 27557

Monday 5:30 - 10:15 pm

W310

Version

- Version 0.1 - 1/24/2022 - second draft
- Version 0.0 - 1/24/2022 - First Draft

Consult the syllabus for Grade Determinants (what everything is worth), Late Policy (don't be late), and Cheating Policy (don't cheat)

Class Schedule

S = Stallings Book / i = Irvine Book

Week #	Date Mon	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
1	1/24	<i>Course Overview</i> S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues i - Ch 1. Basic Concepts	1/24 Arch. Lab i1 - conversions / Logic Tables Assem. Lab i1 - Hello World
2	1/31	S - Ch 9 Number Systems S - Ch 11 Digital Logic i - Ch 2. IA-32 Processor Architecture	1/31 <i>Hw #1 - S1,S2</i> Arch. Lab S9/11 - conversions / Logic Tables Assem. Lab i2 - Register Dump+
3	2/7	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	2/7 <i>Hw #2 - S9,S11</i> Arch. Lab S3 - hyp. machine Assem. Lab i3- Variables
4	2/14	S - Ch 4 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	2/14 <i>Hw #3 - S3</i> Arch. Lab S4 - Cache Assem. Lab i4 - Simple Math, Irvine Library, Stacks
5	2/21	S - Ch 5 Internal Mem. i - Ch 5. Procedures	2/21 <i>Hw #4 - S4</i> Arch. Lab S5 - Hamming Code Assem. Lab i5 - Procedures

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6	2/28	S - Ch 6 External Mem. i - Ch 6. Cond Processing <i>Exam 1 info</i>	2/28 <i>Hw #5 - S5</i> Arch. Lab S6 - External Storage Assem. Lab i6 - Cond. Proc.
	3/7	No Class - Spring Break	
7	3/14	5:30 - 6:45 S - Ch 7 Input/Output i - Ch 7. Integer Arithmetic 7:00 - 8:00 Exam 1 (15%) 8:30 - 10:15 Lab Time	3/14 Hw #6 - S6 Exam 1 (15%)
8	3/21	S - Ch 8 Operating System Support i - Ch 8. Adv. Proc. cont.	3/21 <i>Hw #7 - S7</i> Assem. Lab i7 - iMUL and iDIV
9	3/28	S - Ch 10 Computer Arithmetic i - Ch 9. Strings and Arrays	3/28 <i>Hw #8 - S8</i> Arch. Lab S10 - Booth's Alg. Assem. Lab i8 - Adv. Proc.
10	4/4	S - Ch 14 Processor Structure and Function i - Ch 10. Structures and Macros	4/4 Assem. Lab i9 - 2D Arrays <i>Hw #9 - S10</i>
		Note: 4/8 last day to drop	
11	4/11	Ch 15 Reduced Instruction Set Computers (RISC) i - Ch 12. Floating Point prog. <i>Exam 2 info</i>	4/11 Assem. Lab i10 - STRUCT
12	4/18	5:30-6:15 i - Ch 11. MS-Windows prog 6:30 - 8:30 Exam 2 (15%) 8:30-10:15 Lab Time	4/18 <i>Hw #10 - S14/S15</i> Exam #2

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13	4/25	Topics - TBD <i>Final Exam Info</i>	4/25 Assem. Lab i12 - floating point
14	5/2	<i>Lab time for Assembly Project</i>	5/2 Assembly Project (10%)
		Note: Reading Day 5/4 Final Exam period 5/5 - 5/11	
	5/9	5:30-7:30 Final Exam (20%)	5/9

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