

**CSIT 256-51V Computer Architecture & Assembly Language CRN 27557**  
**Spring 2021 - Class Schedule 0.3**

**Section Info:**

**Monday 5:30 pm-10:15 pm - “Remote Synch” via Zoom**

**Version**

- Version 0.3 - 1/18/2021 - added a missing homework (#10)
- Version 0.2 - 1/18/2021 - changed drop date
- Version 0.1 - 1/14/2021 - corrected final exam and week dates
- Version 0.0 - 1/10/2021 - First Draft

**Consult the syllabus for Grade Determinants (what everything is worth), Late Policy(not accepted late), and Cheating Policy (don’t cheat)**

**S = Stallings Book (Architecture) / I = Irvine Book(Assembly)**

**Class Schedule**

Week #	Date Mon	Topic(s) ( pdfs of slides in Canvas)	Work Due ( details in Canvas )
1	1/25	<i>Course Overview</i> S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues i - Ch 1. Basic Concepts <i>Installing Visual Studio 2019</i> <i>Community and Irvine Library</i>	1/25 <b>Arch. Lab i1 - conversions / Logic Tables</b> Assembly: Hello World
2	2/1	S - Ch 9 Number Systems S - Ch 11 Digital Logic i - Ch 2. IA-32 Processor Architecture	2/1 Hw #1 - S1,S2 <b>Arch. Lab S9/11 - conversions / Logic Tables</b> Assem. Lab i2 - Register Dump+
3	2/8	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	2/8 Hw #2 - S9,S11 <b>Arch. Lab S3 - hyp. machine</b> Assem. Lab i3- Variables
4	2/15	S - Ch 4 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	2/15 Hw #3 - S3 <b>Arch. Lab S4 - Cache</b> Assem. Lab i4 - Simple Math, Irvine Library, Stacks
5	2/22	S - Ch 5 Internal Mem. i - Ch 5. Procedures	2/22 Hw #4 - S4 <b>Arch. Lab S5 - Hamming Code</b> Assem. Lab i5 - Procedures

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<b>6</b>	<b>3/1</b>	S - Ch 6 External Mem. i - Ch 6. Conditional Processing <i>Exam 1 Info</i>	3/1 <i>Hw #5 - S5</i>
	<b>3/8</b>	<b>No Class - Spring Break</b>	
<b>7</b>	<b>3/15</b>	Ch 7 Input/Output i - Ch 7. Integer Arithmetic <b>Exam 1 (15%)</b>	3/15 Hw #6 - S6 Assem. Lab i6 - Cond. Proc. <b>Exam 1 (15%)</b>
<b>8</b>	<b>3/22</b>	S - Ch 8 Operating System Support i - Ch 8. Advanced Procedures	3/22 <i>Hw #7 - S7</i> Assem. Lab i7 - iMUL and iDIV
<b>9</b>	<b>3/29</b>	S - Ch 10 Computer Arithmetic i - Ch 9. Strings and Arrays	3/29 <i>Hw #8 - S8</i> Assem. Lab i8 - Adv. Proc. <b>Arch. Lab S10 - Booth's Alg.</b>
<b>10</b>	<b>4/5</b>	S - Ch 14 Processor Structure and Function i - Ch 10. Structures and Macros	4/5 <i>Hw #9 - S10</i> Assem. Lab i9 - 2D Arrays
		<b>Last day to drop 4/8</b>	
<b>11</b>	<b>4/12</b>	S - Ch 15 Reduced Instruction Set Computers (RISC) i - Ch 12. Floating Point prog. <i>Exam 2 Info</i>	4/12 <i>Hw #10 - S14</i> Assem. Lab i10 - STRUCT
<b>12</b>	<b>4/19</b>	5:30-6:15 Lab Time <b>6:30 - 8:30 Exam 2 (15%)</b> 8:30-10:15 Lab Time	4/19 <b>Exam 2</b> Assem. Lab i12 - floating point
<b>13</b>	<b>4/26</b>	i - Ch 11. MS-Windows prog <i>Final Exam Information</i>	4/26

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<b>14</b>	<b>5/3</b>	<b><i>Lab time for Assembly Project</i></b>	<b>5/3 Assembly Project ( 10% )</b>
		<i>Note: 5/4 is Reading Day</i>	
		<b>Final Exam period 5/5 - 5/11</b>	
	<b>5/10</b>	<b>6:00-8:00 Final Exam (20%)</b>	<b>5/10 Final Exam (20%)</b>

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