

**CSIT 256-51X Computer Architecture & Assembly Language CRN 27557
Spring 2020 - Class Schedule 0.2**

Section Info:

Monday 5:30 pm-10:15 pm West Building W310

Version

- Version 0.2 - 3/18/2020 - revisions due to extended spring break and going online through 4/10
- Version 0.1 - 1/23/2020 - corrected date of final exam
- Version 0.0 - 1/22/2020 - First Release

Consult the syllabus for Grade Determinants (what everything is worth), Late Policy(not accepted late), and Cheating Policy (don't cheat)

S = Stallings Book (Architecture) / I = Irvine Book(Assembly)

Class Schedule

Week #	Date Mon	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
1	1/27	Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues i - Ch 1. Basic Concepts	1/27 Arch. Lab i1 - conversions / Logic Tables Assem. Lab i1 - Hello World
2	2/3	S - Ch 9 Number Systems S - Ch 11 Digital Logic i - Ch 2. IA-32 Processor Architecture	2/3 Hw #1 - S1,S2 Arch. Lab S9/11 - conversions / Logic Tables Assem. Lab i2 - Register Dump+
3	2/10	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	2/10 Hw #2 - S9,S11 Arch. Lab S3 - hyp. machine Assem. Lab i3- Variables
4	2/17	S - Ch 4 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	2/17 Hw #3 - S3 Arch. Lab S4 - Cache Assem. Lab i4 - Simple Math, Irvine Library, Stacks
5	2/24	S - Ch 5 Internal Mem. i - Ch 5. Procedures	2/24 Hw #4 - S4 Arch. Lab S5 - Hamming Code Assem. Lab i5 - Procedures
6	3/2	S - Ch 6 External Mem. i - Ch 6. Conditional Processing	3/2 Hw #5 - S5

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		<i>Exam 1 Info</i>	
	3/9	No Class - Spring Break	
7	3/16	No Class - Extended Spring Break	3/16
8	3/23	<i>online</i> Ch 7 Input/Output i - Ch 7. Integer Arithmetic Exam 1 (15%) [Exam 1 in Canvas]	3/23 Hw #6 - S6 Assem. Lab i6 - Cond. Proc. Exam 1 (15%) [Exam 1 in Canvas]
9	3/30	<i>online</i> S - Ch 8 Operating System Support i - Ch 8. Advanced ProceduresS -	3/30 <i>Hw #7 - S7</i> Assem. Lab i7 - iMUL and iDIV
10	4/6	<i>online</i> Ch 10 Computer Arithmetic i - Ch 9. Strings and Arrays	4/6 <i>Hw #8 - S8</i> Assem. Lab i8 - Adv. Proc. Arch. Lab S10 - Booth's Alg.
	<i>4/10</i>	<i>decision will be made by the school regarding the rest of the semester</i>	<i>The following may change</i>
11	4/13	i - Ch 10. Structures and Macros i - Ch 12. Floating Point prog. <i>Exam 2 Info</i>	4/13 <i>Hw #9 - S10</i> Assem. Lab i9 - 2D Arrays
12	4/20	5:30-6:15 S - Ch 14 Processor Structure and Function 6:30 - 8:30 Exam 2 (15%) (might transition to Canvas) 8:30-10:15 Lab Time	4/20 Exam 2 (may transition to Canvas) Assem. Lab i10 - STRUCT
13	4/27	S - Ch 15 Reduced Instruction Set Computers (RISC) i - Ch 11. MS-Windows prog <i>Final Exam Information</i>	4/27 Assem. Lab i12 - floating point

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Week #	Date Mon	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
14	5/4	<i>Lab time for Assembly Project</i>	5/4 Assembly Project (10%)
		Note: 5/4 last day to drop	
	5/5	<i>Note: Reading Day</i>	
		Final Exam period 5/6 - 5/12	
	5/11	5:30-7:30 Final Exam (20%) (might transition to Canvas)	5/11 Final Exam (20%) (might transition to Canvas)

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