CSIT 256-51 Computer Architecture & Assembly Language CRN 17207
Fall 2025 - Class Schedule 0.2

Monday 5:30 pm-7:50 pm West Building W310
Wednesday 5:30 pm-7:50 pm West Building W310

Version

- Version 0.2 – 10/13/2025 – changes to reflect no class 10/6
- Version 0.1 – 9/3/2025 – First Release
- Version 0.0 - 8/18/2025 - First Draft

Consult the syllabus for Grade Determinants (what everything is worth), Late Policy, Compilation Policy, Starting File Policy and Cheating Policy (don't cheat)

Class Schedule (S = Stallings book, i = Irvine Book)

Week #	Date M	Date W	Topic(s) (pdfs of slides in Canvas)	Work Due M (details in Canvas)	Work Due W
1		**9/3**	Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Concepts i - Ch 1. Basic Concepts		9/3
1	**9/8**		S - Ch 1, Ch 2, cont. i - Ch 1 – cont	9/8 ***Arch. Lab i1 - conversions / Logic Tables***	
2		**9/10**	i - Ch 1 – cont		9/10 Assem. Lab i1 - Hello World
2	**9/15**		S - Ch 10 Number Systems S - Ch 12 Digital Logic i - Ch 2. IA-32 Processor Architecture	9/15 *Hw #1 - S1,S2* ***Arch. Lab S10/12 - conversions / Logic Tables***	
3		**9/17**	i - Ch 2 – cont		9/17 Assem. Lab i2 - Register Dump+
3	**9/22**		S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	9/22 *Hw #2 - S10,S12* ***Arch. Lab S3 - hyp. Machine***	

CSIT 256-51 Computer Architecture & Assembly Language CRN 17207
Fall 2025 - Class Schedule 0.2

Week #	Date M	W	Topic(s) (pdfs of slides in Canvas)	Work Due M (details in Canvas)	W
4		**9/24**	i - Ch 3 – cont		9/24 Assem. Lab i3- Variables
4	**9/29**		S - Ch 4 The Memory Hierarchy: Locality and Performance S - Ch 5 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	9/29 *Hw #3 - S3* ***Arch. Lab S5 – Cache***	
5		**10/1**	i - Ch 4 – cont		10/1 Assem. Lab i4 - Simple Math
5	**10/6**		*No class*	10/6 *n/a*	
6		**10/8**	i - Ch 5. Procedures *Exam 1 Info*		10/8 Hw #4 - S4, S5
6	**10/13**		S - Ch 6 Internal Mem.	10/13 ***Arch. Lab S6 - Hamming Code*** Assem. Lab i5 - Irvine Library and Procedures	
7		**10/15**	5:30 - 6:00 nothing **6:00 - 7:00 Exam 1 (15%)**		10/15 **Exam 1 (15%)**
7	**10/20**		i - Ch 6. Cond Processing i - Ch 7. Integer Arithmetic *Distribute Assembly Project 2 (i1-i7)*	10/20	
8		**10/22**	i - Ch 6 – cont i - Ch 7 – cont		10/22 Hw #5 - S6 Assem. Lab for i6 - Cond. Proc. AND i7 - iMUL and iDIV
8	**10/27**		S - Ch 7 External Mem. S - Ch 8 Input/Output i - Ch 8. Advanced Procedures	10/27 **Arch. Lab S7 - External Storage**	

CSIT 256-51 Computer Architecture & Assembly Language CRN 17207
Fall 2025 - Class Schedule 0.2

Week #	Date M W	Topic(s) (pdfs of slides in Canvas)	Work Due M (details in Canvas) W
9	**10/29**	***Just Lab time for Assembly Project 1*** *Note: during lab time, all questions must be asked in person*	10/29 **Assembly Project 1 (i1-i5)**
9	**11/3**	S - Ch 9 Operating System Support i - Ch 9. Strings and Arrays	11/3 Hw #6 - S7 *Hw #7 - S8* **Arch. Lab S9 – OS**
10	**11/5**	***Just Lab time for Labs i8/i9 and Assembly Project 2*** *Note: during lab time, all questions must be asked in person*	11/5 Assem. Lab i8 - Adv. Proc. Assem. Lab i9 - 2D Arrays **Assembly Project 2 (i1-i7)**
10	**11/10**	S - Ch 11 Computer Arithmetic i - Ch 10. Structures and Macros *Exam 2 Info* *Distribute Assembly Project 3 (i1-i10)*	11/10 *Hw #8 - S9* ***Arch. Lab S11 - Booth's Alg.***
11	**11/12**	i - Ch 10 – cont	11/12 Assem. Lab i10 – STRUCT
11	**11/17**	i - Ch 12. Floating Point prog.	11/17 *Hw #9 - S11*
12	**11/19**	5:30 - 5:50 get ready **5:50 - 7:50 - Exam 2 (25%)**	11/19 **Exam #2 (25%)**
		Note: 11/24 last day to drop	
12	**11/24**	S - Ch 16 Processor Structure and Function	11/24
	11/26	***No class on 11/26*** ***Thanksgiving 11/27***	
13	**12/1**	***Just Lab time for Lab i12 and Assembly Project 3*** *Note: during lab time, all questions must be asked in person*	12/1 Assem. Lab i12 - floating point *Hw #10 - S16*

Week #	Date M	W	Topic(s) (pdfs of slides in Canvas)	Work Due M (details in Canvas)	W
13		**12/3**	i - Ch 11. MS-Windows prog *Final Exam Information*		12/3
14	**12/8**		S - Ch 17 Reduced Instruction Set Computers (RISC)	12/8	
14		**12/10**	***Lab time for Assembly Project 3*** *Note: during lab time, all questions must be asked in person*		12/10 **Assembly Project 3 (i1-i10)**
			Note: Reading Day 12/15 **Final Exam period 12/16 - 12/22**		
		12/17	**5:30-7:30 Final Exam (30%) ****		12/17 **Final Exam (30%)**

* At the end of the semester the lowest Homework grade, the lowest Assembly Lab grade, and the lowest Architecture Lab grade will be dropped

***** To pass the course, you will need an overall average of 60 in the course and you will need a 60 or above for the average of Exam 2 and the Final Exam.***

:)