

CSIT 256-51 Computer Architecture & Assembly Language CRN 17207
Fall 2024 - Class Schedule 0.1

Monday 5:30 pm-7:50 pm West Building W310

Wednesday 5:30 pm-7:50 pm West Building W310

Version

- Version 0.1 – 8/23/2024 – First Release
- Version 0.0 - 8/5/2024 - First Draft

Consult the syllabus for Grade Determinants (what everything is worth), Late Policy, and Cheating Policy (don't cheat)

Class Schedule (S = Stallings book, i = Irvine Book)

Week #	Date M W	Topic(s) (pdfs of slides in Canvas)	Work Due M (details in Canvas) W
1	8/28	Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Concepts	8/28
	9/2	College closed - Labor day 9/2	
1	9/4	i - Ch 1. Basic Concepts	9/4 Arch. Lab i1 - conversions / Logic Tables Assem. Lab i1 - Hello World
2	9/9	S - Ch 10 Number Systems S - Ch 12 Digital Logic i - Ch 2. IA-32 Processor Architecture	9/9 Hw #1 - S1,S2 Arch. Lab S10/12 - conversions / Logic Tables
2	9/11	i - Ch 2 - cont	9/11 Assem. Lab i2 - Register Dump+
3	9/16	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	9/16 Hw #2 - S10,S12 Arch. Lab S3 - hyp. machine
3	9/18	i - Ch 3 - cont	9/18 Assem. Lab i3- Variables
4	9/23	S - Ch 4 The Memory Hierarchy: Locality and Performance S - Ch 5 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	9/23 Hw #3 - S3 Arch. Lab S5 - Cache

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4	9/25	i - Ch 4 - cont	9/25 Assem. Lab i4 - Simple Math
5	9/30	S - Ch 6 Internal Mem. i - Ch 5. Procedures	9/30 <i>Hw #4 - S4, S5</i> Arch. Lab S6 - Hamming Code
5	10/2	i - Ch 5 - cont	10/2 Assem. Lab i5 - Irvine Library and Procedures
6	10/7	S - Ch 7 External Mem. i - Ch 6. Cond Processing <i>Exam 1 Info</i>	10/7 <i>Hw #5 - S6</i> Arch. Lab S7 - External Storage
6	10/9	i - Ch 6 - cont	10/9 Assem. Lab i6 - Cond. Proc.
7	10/14	5:30 - 6:30 S - Ch 8 Input/Output i - Ch 7. Integer Arithmetic 6:50 - 7:50 Exam 1 (15%)	10/14 <i>Hw #6 - S7</i> Exam 1 (15%)
7	10/16	i - Ch 7 - cont	10/16 Assem. Lab i7 - iMUL and iDIV
8	10/21	S - Ch 9 Operating System Support i - Ch 8. Advanced Procedures	10/21 <i>Hw #7 - S8</i> Arch. Lab S9 - OS
8	10/23	i - Ch 8 - cont	10/23 Assem. Lab i8 - Adv. Proc.
9	10/28	S - Ch 11 Computer Arithmetic i - Ch 9. Strings and Arrays	10/28 <i>Hw #8 - S9</i> Arch. Lab S11 - Booth's Alg.
9	10/30	i - Ch 9 - cont	10/30 Assem. Lab i9 - 2D Arrays
10	11/4	S - Ch 16 Processor Structure and Function i - Ch 10. Structures and Macros	11/4 <i>Hw #9 - S11</i>

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10	11/6	i - Ch 10 - cont	11/6 Assem. Lab i10 - STRUCT
11	11/11	S - Ch 17 Reduced Instruction Set Computers (RISC) i - Ch 12. Floating Point prog. <i>Exam 2 Info</i>	11/11 <i>Hw #10 - S16</i>
11	11/13	i - Ch 12 - cont	11/13 Assem. Lab i12 - floating point
		Note: 11/18 last day to drop	
12	11/18	5:30 - 5:50 get ready 5:50 - 7:50 Exam 2 (25%)	11/18 Exam #2 (25%)
12	11/20		11/20
13	11/25	S - TBA i - Ch 11. MS-Windows prog <i>Final Exam Information</i>	11/25
	11/27	No class on 11/27 Thanksgiving 11/28	
13	12/2	Lab time for Assembly Project	12/2
14	12/4	Lab time for Assembly Project	12/4
14	12/9	Lab time for Assembly Project	12/9 Assembly Project (10%)
		Note: Reading Day 12/10 Final Exam period 12/11 - 12/17	
	12/11	5:30-7:30 Final Exam (30%)	12/11 Final Exam (30%)

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