**CSIT 256-51 Computer Architecture & Assembly Language CRN 17207**
**Fall 2022**
**Class Schedule 0.1**

**Tuesday 5:30 pm-7:50 pm West Building W309**
**Thursday 5:30 pm-7:50 pm West Building W309**

**Version**

- Version 0.1 - 8/29/2022 - Changes to reflect no class in person on 9/1
- Version 0.0 - 7/25/2022 - First Draft

**Consult the syllabus for Grade Determinants (what everything is worth), Late Policy, and Cheating Policy (don't cheat)**

**Class Schedule**

| Week # | Date T R | Topic(s) ( pdfs of slides in Canvas) | Work Due T R ( details in Canvas ) |
|---|---|---|---|
| **1** | **9/1** | Material via videos:<br>*Course Overview*<br>*S - Ch 1 Basic Conc. & Comp. Evolution* | 9/1 |
| | | 9/5 College closed - Labor day | |
| **1** | **9/6** | S - Ch 2 Performance Concepts<br>i - Ch 1. Basic Concepts | 9/6<br>***Arch. Lab i1 - conversions / Logic Tables*** |
| **2** | **9/8** | | 9/8<br>Assem. Lab i1 - Hello World |
| **2** | **9/13** | S - Ch 10 Number Systems<br>S - Ch 12 Digital Logic<br>i - Ch 2. IA-32 Processor Architecture | 9/13<br>*Hw #1 - S1,S2*<br>***Arch. Lab S10/12 - conversions / Logic Tables*** |
| **3** | **9/15** | | 9/15<br>Assem. Lab i2 - Register Dump+ |
| **3** | **9/20** | S - Ch 3 A Top-Level View of Computer Function and Interconnection<br>i - Ch 3. Assembly Lang. Fund. | 9/20<br>*Hw #2 - S10,S12*<br>***Arch. Lab S3 - hyp. machine*** |
| **4** | **9/22** | | 9/22<br>Assem. Lab i3- Variables |

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| **4** | **9/27** | S - Ch 4 The Memory Hierarchy: Locality and Performance<br>S - Ch 5 Cache Memory<br>i - Ch 4. Data Tran., Addr., + Arithmetic | 9/27<br>*Hw #3 - S3*<br>***Arch. Lab S5 - Cache*** |
| **5** | **9/29** | | 9/29<br>Assem. Lab i4 - Simple Math |
| **5** | **10/4** | S - Ch 6 Internal Mem.<br>i - Ch 5. Procedures<br>*Exam 1 Info* | 10/4<br>*Hw #4 - S4, S5*<br>***Arch. Lab S6 - Hamming Code*** |
| **6** | **10/6** | | 10/6<br>Assem. Lab i5 - Irvine Library and Procedures |
| **6** | **10/11** | 5:30 - 6:30 S - Ch 7 External Mem.<br>i - Ch 6. Cond Processing<br>**6:50 - 7:50 Exam 1 (15%)** | 10/11<br>*Hw #5 - S6*<br>**Exam 1 (15%)** |
| **7** | **10/13** | | 10/13<br>Assem. Lab i6 - Cond. Proc. |
| **7** | **10/18** | S - Ch 8 Input/Output<br>i - Ch 7. Integer Arithmetic | 10/18<br>Hw #6 - S7 |
| **8** | **10/20** | | 10/20<br>Assem. Lab i7 - iMUL and iDIV |
| **8** | **10/25** | S - Ch 9 Operating System Support<br>i - Ch 8. Advanced Procedures | 10/25<br>*Hw #7 - S8* |
| **9** | **10/27** | | 10/27<br>Assem. Lab i8 - Adv. Proc. |

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|---|---|---|---|
| **9** | **11/1** | S - Ch 11 Computer Arithmetic<br>i - Ch 9. Strings and Arrays | 11/1<br>*Hw #8 - S9*<br>***Arch. Lab S11 - Booth's Alg.*** |
| **10** | **11/3** | | 11/3<br>Assem. Lab i9 - 2D Arrays |
| **10** | **11/8** | S - Ch 16 Processor Structure and Function<br>i - Ch 10. Structures and Macros<br>*Exam 2 Info* | 11/8<br>*Hw #9 - S11* |
| **11** | **11/10** | | 11/10<br>Assem. Lab i10 - STRUCT |
| **11** | **11/15** | 5:30 - 5:50 get ready<br>**5:50 - 7:50 Exam 2 (15%)** | 11/15<br>**Exam #2** |
| | | **Note: 11/16 last day to drop** | |
| **12** | **11/17** | i - Ch 12. Floating Point prog. | 11/17 |
| **12** | **11/22** | S - Ch 17 Reduced Instruction Set Computers (RISC)<br>i - Ch 11. MS-Windows prog<br>*Final Exam Information* | 11/22<br>*Hw #10 - S16*<br>Assem. Lab i12 - floating point |
| | **11/24** | *No class - Thanksgiving* | |
| **13** | **11/29** | ***Lab time for Assembly Project*** | 11/29 |
| **13** | **12/1** | ***Lab time for Assembly Project*** | 12/1 |
| **14** | **12/6** | ***Lab time for Assembly Project*** | 12/6 |
| **14** | **12/8** | ***Lab time for Assembly Project*** | 12/8<br>**Assembly Project ( 10% )** |
| | | **Note: Reading Day 12/13**<br>**Final Exam period 12/14 - 12/20** | |

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|---|---|---|---|---|---|
| | **12/15** | | **5:30-7:30 Final Exam (20%)** | | 12/15 **Final Exam (20%)** |

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