

CSIT 256-51 Computer Architecture & Assembly Language CRN 17207

Fall 2021

Class Schedule 0.2

Thursday 5:30 pm-10:15 pm West Building W309

Version

- Version 0.2 - 9/7/2021 - changes to reflect no class on 9/2
- Version 0.1 - 8/30/2021 - First Release
- Version 0.0 - 7/20/2021 - First Draft

Consult the syllabus for Grade Determinants (what everything is worth), Late Policy, and Cheating Policy (don't cheat)

Class Schedule

Week #	Date Thur	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
1	9/2	<i>No Class - College Closed</i>	
2	9/9	Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues i - Ch 1. Basic Concepts	9/9 Arch. Lab i1 - conversions / Logic Tables Assem. Lab i1 - Hello World
3	9/16	S - Ch 9 Number Systems S - Ch 11 Digital Logic i - Ch 2. IA-32 Processor Architecture	9/16 <i>Hw #1 - S1,S2</i> Arch. Lab S9/11 - conversions / Logic Tables Assem. Lab i2 - Register Dump+
4	9/23	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	9/23 <i>Hw #2 - S9,S11</i> Arch. Lab S3 - hyp. machine Assem. Lab i3- Variables
5	9/30	S - Ch 4 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	9/30 <i>Hw #3 - S3</i> Arch. Lab S4 - Cache Assem. Lab i4 - Simple Math, Irvine Library, Stacks
6	10/7	S - Ch 5 Internal Mem. S - Ch 6 External Mem. i - Ch 5. Procedures	10/7 <i>Hw #4 - S4</i> Arch. Lab S5 - Hamming Code Assem. Lab i5 - Procedures

CSIT 256-51 Computer Architecture & Assembly Language CRN 17207

Fall 2021

Class Schedule 0.2

Week #	Date Thur	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
7	10/14	5:30 - 6:45 S - Ch 7 Input/Output i - Ch 6. Cond Processing 7:00 - 8:00 Exam 1 (15%) 8:30 - 10:15 Lab Time	10/14 <i>Hw #5 - S5</i> <i>Hw #6 - S6</i> Exam 1 (15%)
8	10/21	S - Ch 8 Operating System Support i - Ch 7. Integer Arithmetic i - Ch 8. Advanced Procedures	10/21 <i>Hw #7 - S7</i> Assem. Lab i6 - Cond. Proc.
9	10/28	i - Ch 8. Adv. Proc. cont. i - Ch 9. Strings and Arrays	10/28 <i>Hw #8 - S8</i> Assem. Lab i7 - iMUL and iDIV Assem. Lab i8 - Adv. Proc.
10	11/4	S - Ch 10 Computer Arithmetic i - Ch 10. Structures and Macros	11/4 Assem. Lab i9 - 2D Arrays Arch. Lab S10 - Booth's Alg.
11	11/11	i - Ch 12. Floating Point prog. <i>Exam 2 Info</i>	11/11 <i>Hw #9 - S10</i> Assem. Lab i10 - STRUCT
		Note: 11/17 last day to drop	
12	11/18	5:30-6:15 S - Ch 14 Processor Structure and Function 6:30 - 8:30 Exam 2 (15%) 8:30-10:15 Lab Time	11/18 Exam #2
	11/25	<i>No class - Thanksgiving</i>	
13	12/2	S - Ch 15 Reduced Instruction Set Computers (RISC) i - Ch 11. MS-Windows prog <i>Final Exam Information</i>	12/2 Assem. Lab i12 - floating point
14	12/9	Lab time for Assembly Project	12/9 Assembly Project (10%)
		Note: Reading Day 12/14 Final Exam period 12/15 - 12/21	

CSIT 256-51 Computer Architecture & Assembly Language CRN 17207

Fall 2021

Class Schedule 0.2

Week #	Date Thur	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
---------------	----------------------	---	--

	12/16	5:30-7:30 Final Exam (20%)	12/16 Final Exam (20%)
--	--------------	-----------------------------------	-------------------------------

:)