CSIT 256-51v Computer Architecture & Assembly Language CRN 17207
Fall 2020
Class Schedule 0.2 (draft)

Thursday 5:30 pm-10:15 pm "Remote Sync"

Version

- Version 0.2 - 8/23/2020 - still a draft, but published
- Version 0.1 - 8/17/2020 - Topics/assignments added
- Version 0.0 - 8/16/2020 - just shell of dates

Consult the syllabus for Grade Determinants (what everything is worth), Late Policy, and Cheating Policy (don't cheat)

Class Schedule

Week #	Date Thursday	Topic(s) (pdfs of slides in Canvas)	Work Due Thursday (details in Canvas)
1	**9/3**	***Course Overview*** S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues i - Ch 1. Basic Concepts	9/3 Arch. Lab i1 - conversions / Logic Tables ***prepare computers with Visual Studio 2019 (test with sample project)***
		9/7 College closed - Labor day	
2	**9/10**	S - Ch 9 Number Systems S - Ch 11 Digital Logic i - Ch 2. x86 Proc. Architecture	9/10 *Hw #1 - S1,S2* Arch. Lab S9/11 - conversions / Logic Tables **Assem. Lab for i2** - Hello World and Registers
3	**9/17**	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	9/17 *Hw #2 - S9,S11* Arch. Lab S3 - hyp. machine **Assem. Lab for i3**- Variables

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4	**9/24**	S - Ch 4 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	9/24 *Hw #3 - S3* Arch. Lab S4 - Cache **Assem. Lab for i4** - simp math
5	**10/1**	S - Ch 5 Internal Mem. Technology i - Ch 5. Procedures	10/1 *Hw #4 - S4* Arch. Lab S5 - Hamming Code **Assem. Lab for i5** - Procedures
6	**10/8**	***Exam 1 Information*** S - Ch 6 External Mem. i - Ch 6. Conditional Processing	10/8 *Hw #5 - S5* **Assem. Lab for i6** - Cond. Proc.
7	**10/15**	**Exam 1 (15%)** S - Ch 7 Input/Output i - Ch 7. Integer Arithmetic	10/15 **Exam 1 (15%)** *Hw #6 - S6*
8	**10/22**	S - Ch 8 Operating System Support i - Ch 8. Advanced Procedures	10/22 *Hw #7 - S7* **Assem. Lab for i7** - iMUL and iDIV

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9	**10/29**	S - Ch 10 Computer Arithmetic i - Ch 9. Strings and Arrays	10/29 *Hw #8 - S8* Arch. Lab S10 - Booth **Assem. Lab for i8** - Adv. Proc
10	**11/5**	S - Ch 14 Processor Structure and Function i - Ch 10. Structures and Macros	11/5 *Hw #9 - S10* **Assem. Lab for i9** - 2D Arrays
11	**11/12**	***Exam 2 info*** S - Ch 15 Reduced Instruction Set Computers (RISC) i - Ch 12. Floating Point Processing and Instruction Encoding	11/12 *Hw #10 - S14* **Assem. Lab for i10** - STRUCT
		Note: 11/13 last day to drop	
12	**11/19**	**Exam 2 (15 %)**	11/19 **Exam 2 (15%)** **Assem. Lab for i12** - floating point
	11/26	***No class - Thanksgiving 11/26***	11/26 - n/a
13	**12/3**	Final Exam Information i - Ch 11. MS-Windows prog	12/3

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14	**12/10**	***Lab Time for Final Project***	12/10 **Assembly Project (10%)**
		Reading Day 12/15 **Final Exam period 12/16 - 12/22**	
	12/17	**6:00 - 8:00** **Final Exam (20 %)**	12/17 **Final Exam (20%)**

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