

CISY 256-51x Computer Architecture & Assembly Language CRN 20503
Spring 2019
Class Schedule 0.3

Section Info:

Monday 5:30 pm - 10:15 pm West Building W310

Version

- Version 0.3 - 4/8/209 - changes to reflect abbreviated class on 4/8
- Version 0.1 - 4/8/2019 - changes to reflect no class 4/1
- Version 0.0 - 1/26/2019 - First Release

Consult syllabus for Grade Determinants (what everything is worth), Late Policy (-10 pts calendar day, max 7 days), Cheating Policy (don't cheat), and Life Happens Policy (1 "Life Happens" for each category: Lab, Hw, Project, no "Life Happens" after 5/6)

Class Schedule [S = Stallings Architecture Book, i = Irvine Assembly Book]

Week #	Date Mon	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
1	1/28	<i>Course Overview</i> S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues i - Ch 1. Basic Concepts	1/28 Arch. Lab i1 - conversions / Logic Tables Assem. Lab i1 - Hello World
2	2/4	S - Ch 9 Number Systems S - Ch 11 Digital Logic i - Ch 2. IA-32 Processor Architecture	2/4 <i>Hw #1 - S1,S2</i> Arch. Lab S9/11 - conversions / Logic Tables Assem. Lab i2 - Register Dump+
3	2/11	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	2/11 <i>Hw #2 - S9,S11</i> Arch. Lab S3 - hyp. machine Assem. Lab i3- Variables
4	2/18	S - Ch 4 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	2/18 <i>Hw #3 - S3</i> Arch. Lab S4 - Cache Assem. Lab i4 - Simple Math, Irvine Library, Stacks
5	2/25	S - Ch 5 Internal Mem. i - Ch 5. Procedures	2/25 <i>Hw #4 - S4</i> Arch. Lab S5 - Hamming Code Assem. Lab i5 - Procedures

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6	3/4	S - Ch 6 External Mem. i - Ch 6. Conditional Processing	3/4 <i>Hw #5 - S5</i> Assem. Lab i6 - Cond. Proc.
	3/11	No class - Spring Break	
7	3/18	S - Ch 7 Input/Output i - Ch 7. Integer Arithmetic <i>Exam 1 Review</i>	3/18 <i>Hw #6 - S6</i>
8	3/25	S - Ch 8 Operating System Support i - Ch 8. Advanced Procedures Exam 1 (15%)	3/25 <i>Hw #7 - S7</i> Exam 1 (15%)
9	4/1	No-Class	4/1 n/a
10	4/8	Brief Intro: S - Ch 10 Computer Arithmetic i - Ch 9. Strings and Arrays	4/8 n/a
		Note: 4/8 last day to drop	
11	4/15	S - Ch 10 Computer Arithmetic i - Ch 9. Strings and Arrays <i>Exam 2 Info</i>	4/15 <i>Hw #8 - S8</i> Arch. Lab S10 - Booth's Alg. Assem. Lab i7 - iMUL and iDIV
12	4/22	5:30-6:15 brief lecture i10 6:30 - 8:30 Exam 2 (15%) 8:30-10:15 Lab Time -- i - Ch 10. Structures and Macros	4/22 Exam 2 (15%) <i>Hw #9 - S10</i> Assem. Lab i9 - 2D Arrays
13	4/29	i - Ch 12. Floating Point prog. i - Ch 11. MS-Windows prog <i>Final Exam Information</i>	4/29 Assem. Lab i10 - STRUCT
14	5/6	Lab time for Assembly Project	5/6 Assembly Project (10%)
		Reading Day 5/7 Final Exam period 5/8 - 5/14	

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	5/13	Final Exam (20%)	5/13 Final Exam (20%)

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