

CISY 256-51X Computer Architecture & Assembly Language CRN 20503

Spring 2018

Class Schedule 0.3 [draft]

Section Info:

CRN 20503, Monday 5:30 - 10:15 West Building W310

Version

- Version 0.3 - 3/4/2018 - changes to timing of assembly labs for i4,i5,i6,i7
- [Version 0.2 changes to reflect no class 2/19](#)
- [Version 0.1 - 1/6/2018 - filled in topics](#)
- Version 0.0 - 1/2/2018 - Empty Grid

Consult syllabus for Grade Determinants (what everything is worth), Late Policy (-10 pts calendar day, max 7 days), Cheating Policy (don't cheat), and Life Happens Policy (1 "Life Happens" for each category: Lab, ArchLab, Hw. no "Life Happens" after 4/23)

Class Schedule

Week #	Date Monday	Topic(s) (pdfs of slides in WebStudy)	Work Due (details in WebStudy)
1	1/22	Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues i - Ch 1. Basic Concepts	1/22 Arch. Lab i1 - conversions/Logic
2	1/29	S - Ch 9 Number Systems S - Ch 11 Digital Logic i - Ch 2. IA-32 Processor Architecture	1/29 <i>Hw #1 - S1,S2</i> Arch. Lab S9/11 - conversions/Logic Assem. Lab i2 - Hello World+
3	2/5	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	2/5 <i>Hw #2 - S9,S11</i> Arch. Lab S3 - hyp. machine Assem. Lab i3- Variables
4	2/12	S - Ch 4 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	2/12 <i>Hw #3 - S3</i>
5	2/19	No Class	2/19 n/a

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6	2/26	S - Ch 5 Internal Mem. i - Ch 5. Procedures i - Ch 6. Conditional Processing <i>Exam 1 Review</i>	2/26 <i>Hw #4 - S4</i> Arch. Lab S4 - Cache
7	3/5	5:30 - 7:15 S - Ch 6 External Mem. i - Ch 6. Cond Proc cont. 7:15 - 7:30 Break 7:30 - 8:30 Exam 1 (15%) 8:30 - 10:15 Lab Time	3/5 <i>Hw #5 - S5</i> Assem. Lab i4/i5 - Simple Math, Irvine Library, Stacks Exam 1 (15%)
	3/12	No class - Spring Break	
8	3/19	S - Ch 7 Input/Output S - Ch 8 Operating System Support i - Ch 7. Integer Arithmetic	3/19 <i>Hw #6 - S6</i> Arch. Lab S5 - Hamming Code Assem. Lab i6 - Cond.I Proc.
9	3/26	S - Ch 10 Computer Arithmetic i - Ch 8. Advanced Procedures i - Ch 9. Strings and Arrays	3/26 / <i>Hw #7 - S7 \</i> \ <i>Hw #8 - S8 /</i> Arch. Lab S10 - Booth's Algorithm Assem. Lab i7 - iMUL and iDIV
10	4/2	i - Ch 10. Structures and Macros i - Ch 12. Floating Point Programming <i>Exam 2 Info</i>	4/2 <i>Hw #9 - S10</i> Assem. Lab i9 - 2D Arrays
Note:	4/6	Last day to withdraw, audit,	
11	4/9	5:30 - 6:15 i - Ch 11. MS-Windows Programming 6:15 - 6:30 Break 6:30 - 8:30 Exam 2 (15%) 8:30 - 10:15 Lab Time	4/9 Exam 2 (15%) Assem. Lab i10 - STRUCT AND i12 - Floating Point (get real!)
12	4/16	S - Ch 14 Processor Structure and	4/16

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		Function S - Ch 15 Reduced Instruction Set Computers (RISC) <i>Final Exam Information</i>	
13	4/23	Lab time for Assembly Project	4/23
14	4/30	Lab time for Assembly Project	4/30 <i>Assembly Project (10%)</i>
		Final Exams Week of 5/7	
	5/7	6:00 - 8:00 Final Exam (20%)	Final Exam (20%)

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