

**CISY 256-51x Computer Architecture & Assembly Language**  
**Spring 2017 - CRN 20503**  
**Class Schedule v0.1**

**M 5:30 pm - 10:15 pm West Building W310**

**Class Schedule Version 0.0** (For latest update see WebStudy)

**Versions:**

- Version 0.1 - 12/29/2016 - Architecture Labs added
- Version 0.0 - 12/27/2016 - Shell of dates/topics/work due

**S = Stallings Architecture Book, I = Irvine Assembly Book**

See WebStudy for Homework(15%) / Lab(25%) & Project(10%) Details

<b>Week</b>	<b>Date</b>	<b>Topic(s)</b>	<b>Work Due</b>
1	1/23	Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues I - Ch 1. Basic Concepts	<b>Arch. Lab I1 - conversions/Logic</b>
2	1/30	S - Ch 9 Number Systems S - Ch 11 Digital Logic I - Ch 2. IA-32 Processor Architecture	<i>Hw #1 - S1,S2</i> <b>Arch. Lab S9/I1 - conversions/Logic</b> Assem. Lab I2 - Hello World+
3	2/6	S - Ch 3 A Top-Level View of Computer Function and Interconnection I - Ch 3. Assembly Lang. Fund.	<i>Hw #2 - S9,S11</i> <b>Arch. Lab S3 - hyp. machine</b> Assem. Lab I3- Variables
4	2/13	S - Ch 4 Cache Memory I - Ch 4. Data Tran., Addr., + Arithmetic	<i>Hw #3 - S3</i> <b>Arch. Lab S4 - Cache</b> Assem. Lab I4 - Simple Math, Irvine Library, Stacks
5	2/20	S - Ch 5 Internal Mem. I - Ch 5. Procedures	<i>Hw #4 - S4</i> <b>Arch. Lab S5 - Hamming Code</b> Assem. Lab I5 - Procedures
6	2/27	S - Ch 6 External Mem. I - Ch 6. Conditional Processing <i>Exam 1 Review</i>	<i>Hw #5 - S5</i> Assem. Lab I6 - Conditional Processing

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	3/6	Spring Break 3/6 - 3/11	
7	3/13	S - Ch 7 Input/Output I - Ch 7. Integer Arithmetic <b>Exam 1 (15%)</b>	<i>Hw #6 - S6</i> <b>Exam 1 (15%)</b> Assem. Lab I7 - iMUL and iDIV
8	3/20	S - Ch 8 Operating System Support I - Ch 8. Advanced Procedures	<i>Hw #7 - S7</i>
9	3/27	S - Ch 10 Computer Arithmetic I - Ch 9. Strings and Arrays	<i>Hw #8 - S8</i> <b>Arch. Lab S10 - Booth's Algorithm</b> Assem. Lab I9 - 2D Arrays
10	4/3	S - Ch 14 Processor Structure and Function I - Ch 10. Structures and Macros	<i>Hw #9 - S10</i> Assem. Lab I10 - STRUCT
		<i>Note: 4/7 is last day to withdraw</i>	
11	4/10	S - Ch 15 Reduced Instruction Set Computers (RISC) I - Ch 12. Floating Point Programming <i>Exam 2 Info</i>	Assem. Lab I12 - Floating Point (get real!)
12	4/17	<b>5:30 - 6:00 Lab Time</b> <b>6:00 - 8:00 Exam 2 (15%)</b> <b>8:00 - 10:15 Lab Time</b>	<b>Exam 2 (15%)</b>
13	4/24	I - Ch 11. MS-Windows Programming <i>Final Exam Information</i>	
14	5/1	<b>Lab time for Assembly Project</b>	<b>Assembly Project ( 10% )</b>
		<i>Note: 5/2 is Reading Day</i>	
	5/8	<b>6:00 - 8:00 Final Exam ( 20% )</b>	

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