

CISY 256-51x Computer Architecture & Assembly Language
Spring 2016 - CRN 20503
Class Schedule v0.7

M 5:30 pm - 10:15 pm West Building W310

Class Schedule Version 0.7 (For latest update see Lion's Den)

Versions:

- Version 0.7 - 4/17/2016 - postponement of 'Exam 2'
- [Version 0.6 - 3/26/2016 - Changes to reflect no class 3/21](#)
- [Version 0.5 - 3/15/16 - changes to reflect postponement of exam 1](#)
- [Version 0.4 - 2/22/2016 - Changes to reflect no class 2/15](#)
- [Version 0.3 - 2/7/2016 - changes to reflect no class 2/1](#)
- [Version 0.2 - 1/25/2016 - changes to reflect no class 1/25 due to "lingering effects of the snowstorm"](#)
- [Version 0.1 - 1/19/2016 - added topics](#)
- Version 0.0 - 12/29/2015 - Shell of dates

S = Stallings Architecture Book, I = Irvine Assembly Book

See *Lion's Den* for Homework(15%) / Lab(25%) & Project(10%) Details

Week	Date	Topic(s)	Work Due
1	1/25	<i>no class</i>	
2	2/1	<i>no class</i>	
3	2/8	Overview: https://youtu.be/lxAwlNMWT-k S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues I - Ch 1. Basic Concepts	Lab 1 - conversions
4	2/15	<i>no class</i>	
5	2/22	S - Ch 9 Number Systems S - Ch 11 Digital Logic I - Ch 2. IA-32 Processor Architecture	<i>Hw #1 - S1,S2</i> Lab 2 - Hello World+
6	2/29 (Yes it's a leap year)	S - Ch 3 A Top-Level View of Computer Function and Interconnection S - Ch 4 Cache Memory I - Ch 3. Assembly Lang. Fund. I - Ch 4. Data Tran., Addr., + Arithmetic	<i>Hw #2 - S9,S11</i> Lab 3 - Variables
	3/7	Spring Break	
7	3/14	S - Ch 5 Internal Mem. S - Ch 6 External Mem. I - Ch 4. Data Tran..., cont. I - Ch 5. Procedures	<i>Hw #3 - S3, S4</i> Lab 4 - Simple Math, Irvine Library, Stacks

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8	3/21	No Class	
9	3/28	S - Ch 7 Input/Output S - Ch 8 Operating System Support I - Ch 6. Conditional Processing Exam 1 alternate (15%)	Hw #4 - S5, S6 Lab 5 - catch up
10	4/4	<i>Exam 1 Review</i> S - Ch 10 Computer Arithmetic I - Ch 7. Integer Arithmetic	Hw #5 - S7, S8 Lab 6 - Conditional Processing
11	4/11	S - Ch 14 Processor Structure and Function I - Ch 8. Advanced Procedures I - Ch 9. Strings and Arrays <i>Exam 2 Info</i>	Hw #6 - S10 Lab 7 - iMUL and iDIV (change Lab 4 to include product, quotient, remainder)
12	4/18	S - Ch 15 Reduced Instruction Set Computers (RISC) I - Ch 10. Structures and Macros	
13	4/25	I - Ch 11. MS-Windows Programming I - Ch 12. Floating Point Programming <i>Final Exam Information</i>	Lab 8 - 2D Arrays+
14	5/2	Lab time for: Exam 2 and Project	Exam 2 (15%) Assembly Project (10%)
	5/9	6:00 - 8:00 Final Exam (20%)	

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