

CISY 256-51X Computer Architecture & Assembly Language CRN 14753

Fall 2018

Class Schedule 0.3

Section Info:

Thursday 5:30-10:15 West Building W309

Version

- Version 0.3 - 11/29/2018 - changes to reflect no class 11/15
- [Version 0.2 - 11/8/2018 - changes to reflect no class 11/1](#)
- [Version 0.1 - 8/28/2018 - Filled in topics](#)
- Version 0.0 - 7/23/2018 - Empty Grid

Consult syllabus for Grade Determinants (what everything is worth), Late Policy (-10 pts calendar day, max 7 days), Cheating Policy (don't cheat), and Life Happens Policy (1 "Life Happens" for each category: Labs, Homework, no "Life Happens" after 11/29)

Class Schedule [S = Stallings Architecture Book, i = Irvine Assembly Book]

Week #	Date Thur	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
1	8/30	Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues i - Ch 1. Basic Concepts	8/30 Arch. Lab i1 - conversions/Logic
2	9/6	S - Ch 9 Number Systems S - Ch 11 Digital Logic i - Ch 2. IA-32 Processor Architecture	9/6 <i>Hw #1 - S1,S2</i> Arch. Lab S9/11 - conversions/Logic Assem. Lab i2 - Hello World+
3	9/13	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	9/13 <i>Hw #2 - S9,S11</i> Arch. Lab S3 - hyp. machine Assem. Lab i3- Variables
4	9/20	S - Ch 4 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	9/20 <i>Hw #3 - S3</i> Arch. Lab S4 - Cache Assem. Lab i4 - Simple Math, Irvine Library, Stacks
5	9/27	S - Ch 5 Internal Mem. i - Ch 5. Procedures	9/27 <i>Hw #4 - S4</i> Arch. Lab S5 - Hamming Code Assem. Lab i5 - Procedures

CISY 256-51X Computer Architecture & Assembly Language CRN 14753

Fall 2018

Class Schedule 0.3

Week #	Date Thur	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
6	10/4	S - Ch 6 External Mem. i - Ch 6. Conditional Processing <i>Exam 1 Review</i>	10/4 <i>Hw #5 - S5</i> Assem. Lab i6 - Cond. Proc.
7	10/11	S - Ch 7 Input/Output i - Ch 7. Integer Arithmetic <i>Exam 1 (15%)</i>	10/11 <i>Hw #6 - S6</i> <i>Exam 1 (15%)</i>
8	10/18	S - Ch 8 Operating System Support i - Ch 8. Advanced Procedures	10/18 <i>Hw #7 - S7</i> Assem. Lab i7 - iMUL and iDIV
9	10/25	S - Ch 10 Computer Arithmetic i - Ch 9. Strings and Arrays	10/25 <i>Hw #8 - S8</i> Arch. Lab S10 - Booth's Alg.
10	11/1	No class	
11	11/8	i - Ch 10. Structures and Macros i - Ch 12. Floating Point Programming <i>Exam 2 Info</i>	11/8 <i>Hw #9 - S10</i>
		Note: 11/9 last day to drop	
12	11/15	No class - snow	11/15
	11/22	No class - thanksgiving	
13	11/29	5:30-6:15 S - Ch 14 Processor Structure and Function S - Ch 15 RISC i - Ch 11. MS-Windows Programming <i>Final Exam Information</i> 6:30 - 8:30 Exam 2 (15%) 8:30-10:15 Lab Time	11/29 Assem. Lab i9 - 2D Arrays <i>Exam 2 (15%)</i>
14	12/6	Lab time for Assembly Project	12/6 Assem. Lab i10 - STRUCT AND i12 - Floating Point (get real!) <i>Assembly Project (10%)</i>

CISY 256-51X Computer Architecture & Assembly Language CRN 14753

Fall 2018

Class Schedule 0.3

Week #	Date Thur	Topic(s) (pdfs of slides in Canvas)	Work Due (details in Canvas)
		Reading Day 12/11 Final Exam period 12/12 - 12/18	
	12/13	Final Exam (20%)	12/13 Final Exam (20%)

:)