

CISY 256-51X Computer Architecture & Assembly Language CRN 14753

Fall 2017

Class Schedule 0.2

Section Info:

Wednesday 5:30 pm - 10:15 pm West Building W311

Version

- Version 0.2 - 11/14/2017 - changes to night of Exam 2 and Final Exam
- [Version 0.1 - 8/13/2017 - added topics and assignments](#)
- Version 0.0 - 8/13/2017 - empty grid

Consult syllabus for Grade Determinants (what everything is worth), Late Policy (-10 pts calendar day, max 7 days), Cheating Policy (don't cheat), and Life Happens Policy (1 "Life Happens" for each category: Lab, Hw, no "Life Happens" after 12/6)

Class Schedule [S = Stallings Architecture Book, i = Irvine Assembly Book]

Week #	Date Wed	Topic(s) (pdfs of slides in WebStudy)	Work Due (details in WebStudy)
1	9/6	Course Overview S - Ch 1 Basic Conc. & Comp. Evolution S - Ch 2 Performance Issues i - Ch 1. Basic Concepts	9/6 Arch. Lab i1 - conversions/Logic
2	9/13	S - Ch 9 Number Systems S - Ch 11 Digital Logic i - Ch 2. IA-32 Processor Architecture	9/13 <i>Hw #1 - S1,S2</i> Arch. Lab S9/11 - conversions/Logic Assem. Lab i2 - Hello World+
3	9/20	S - Ch 3 A Top-Level View of Computer Function and Interconnection i - Ch 3. Assembly Lang. Fund.	9/20 <i>Hw #2 - S9,S11</i> Arch. Lab S3 - hyp. machine Assem. Lab i3- Variables
4	9/27	S - Ch 4 Cache Memory i - Ch 4. Data Tran., Addr., + Arithmetic	9/27 <i>Hw #3 - S3</i> Arch. Lab S4 - Cache Assem. Lab i4 - Simple Math, Irvine Library, Stacks
5	10/4	S - Ch 5 Internal Mem. i - Ch 5. Procedures	10/4 <i>Hw #4 - S4</i> Arch. Lab S5 - Hamming Code Assem. Lab i5 - Procedures

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6	10/11	S - Ch 6 External Mem. i - Ch 6. Conditional Processing <i>Exam 1 Review</i>	10/11 <i>Hw #5 - S5</i> Assem. Lab i6 - Conditional Processing
7	10/18	S - Ch 7 Input/Output i - Ch 7. Integer Arithmetic <i>Exam 1 (15%)</i>	10/18 <i>Hw #6 - S6</i> <i>Exam 1 (15%)</i>
8	10/25	S - Ch 8 Operating System Support i - Ch 8. Advanced Procedures	10/25 <i>Hw #7 - S7</i> Assem. Lab i7 - iMUL and iDIV
9	11/1	S - Ch 10 Computer Arithmetic i - Ch 9. Strings and Arrays	11/1 <i>Hw #8 - S8</i> Arch. Lab S10 - Booth's Algorithm Assem. Lab i9 - 2D Arrays
10	11/8	i - Ch 10. Structures and Macros i - Ch 12. Floating Point Programming <i>Exam 2 Info</i>	11/8 <i>Hw #9 - S10</i>
11	11/15	5:30 - 10:15 Exam 2 (15%)	11/15 <i>Exam 2 (15%)</i>
		<i>Note: 11/17 last day to withdraw</i>	
	11/22	No class - Thanksgiving	
12	11/29	S - Ch 14 Processor Structure and Function S - Ch 15 Reduced Instruction Set Computers (RISC) i - Ch 11. MS-Windows Programming <i>Final Exam Information</i>	11/29 Assem. Lab i10 - STRUCT AND i12 - Floating Point (get real!)
13	12/6	Lab time for Assembly Project	12/6 n/a

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14	12/13	Last Lab time for Assembly Project	12/13 <i>Assembly Project (10%)</i>
	12/20	5:30 - 7:30 Final Exam (20%)	12/20 Final Exam (20%)

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